

## ,IN THE UNITED STATES-PATENT AND TRADEMARK OFFICE

APPLN. OF: USAMI

SERIAL NO.: 10/695,159

FILED:

October 28, 2003

FOR:

Semiconductor Device and Method of Manufacturing the Same

DOCKET:

**NEC 01207 DIV2** 

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

In connection with the above-entitled matter, Applicant wishes to bring to the attention of the Patent Office additional art received in connection with an Office Action in the corresponding parent application, U.S. Serial No. 10/260947. Attached are copies of U.S. Patent Office Form PTO-892 and the Office Action dated March 10, 2004. Since the references were cited in the related parent application, additional copies are not believed necessary. The claims in the present application are believed to be patentably distinguished over these references.

This Information Disclosure Statement is being made pursuant to the duty of disclosure imposed by law and formulated in 37 CFR 1.56(A). No representation is made that the information thus disclosed in fact constitute prior art or that it is the closest prior art, inasmuch as 37 CFR 1.56(A) relies on a materiality concept which depends on subjectivity.

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The enclosed Supplemental Information Disclosure Statement is being submitted within three months of receipt of the Office Action in the related divisional application, as certified in the attached Certification for Information Disclosure Statement. Therefore, we believe there are no fees involved with this prior art disclosure statement. However, in the event there are any fees payable, please charge them to our Deposit Account No. 08-1391.

Respectfully submitted,

Norman P. Soloway Attorney for Applicant Registration No. 24,315

## **CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on Worch 18, 2004 at Tucson, Arizona.

By:

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STATEMENT UNDER 37 CFR 1.97(e) ACCOMPANYING INFORMATION DISCLOSURE STATEMENT		Docket No. NEC 01207 DIV 2
In Re Application Of: USAMI MAR 2 2 2004		
Serial No.  10/695,159  October 28, 2003	Examiner	Group Art Unit
Invention: Semiconductor Device and Method of Manufacturing the Same		
TO THE ASSISTANT COMMISSIONER FOR PATENTS:		
This is a statement under the provisions of 37 CFR 1.97(e) in the above-identified application.		
Check applicable statement herebelow:		
Statement Under 37 CFR 1.97(e)(1)		
Each item of information contained in the accompanying Information Disclosure Statement was first cited in a related U.S. application not more than three months prior to the filing of the Information Disclosure Statement.		
Statement Under 37 CFR 1.97(e)(2)		
No item of information contained in the accompanying Information Disclosure Statement was cited in a communication in a related U.S. application, and, to the knowledge of the undersigned person, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.		
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I certify that this document is being deposited on with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Signature of Person Mailing Correspondence

Kathryn Grimes

Typed or Printed Name of Person Mailing Correspondence